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Application Number	10/596,944
Filing Date	06/29/2006
First Named Inventor	Robert L. MAZIASZ
Art Unit	2825
Examiner Name	Patrick SANDOVAL
Attorney Docket Number	SC12656TS

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Assignee of record of the entire interest. See 37 CFR 3.71.				
Statement under 37 GFR 3.73(b) is enclosed. (Form PTO/Sb/96)				
SIGNATURE of Applicant or Assignee of Record				
Signature / 1 - of /				
Name Pavid G. Dolezal, U.S. Patent Outsourcing Manager, Freescale Semiconductor, Inc.				
Date 872	Telephor	(012) 000 0000		
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.				
Total offorms are submitted.				

This collection of information is required by 37 CFR 1.36. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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STATEMENT UNDER 37 CFR 3.73(b)			
Applicant/Patent Owner: Freescale Semiconductor, Inc.			
Application No./Patent No./Control No.: 10/596.944	_ Filed/Issue Date: June 29, 2006		
Entitled: CIRCUIT LAYOUT COMPACTION USING RESHAPING			
Freescale Semiconductor, Inc.	, a corporation		
(Name of Assignee)	(Type of Assignee: corporation, partnership, university, government agency, etc.)		
states that it is:  1.  the assignee of the entire right, title, and interest; or			
an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is	%)		
in the patent application/patent identified above by virtue of either:			
A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel <u>017998</u> , Frame <u>0858</u> , or a true copy of the original assignment is attached.			
OR  B. A chain of title from the inventor(s), of the patent applicat	on/patent identified above, to the current assignee as follows:		
1. From: To	•		
1. From: To: To: The document was recorded in the United States Patent and Trademark Office at Reel, Frame, or for which a copy thereof is attached.			
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3. From:To			
3. From:			
Additional documents in the chain of title are listed on a supplemental sheet.			
As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.  [NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]			
The undersigned (whose title is supplied below) is authorized to	act on behalf of the assignee. \$128/08		
Signature	Date		
David G. Dolezal	(512) 996-6839		
Printed or Typed Name	Telephone Number		
U.S. Patent Outsourcing Manager, Freescale Semiconductor, Inc.			
Title			

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